

AUS920030325US1

Lee

Improved Technique for System Initial Program Load
or Boot-Up of Electronic Devices and Systems

1/4

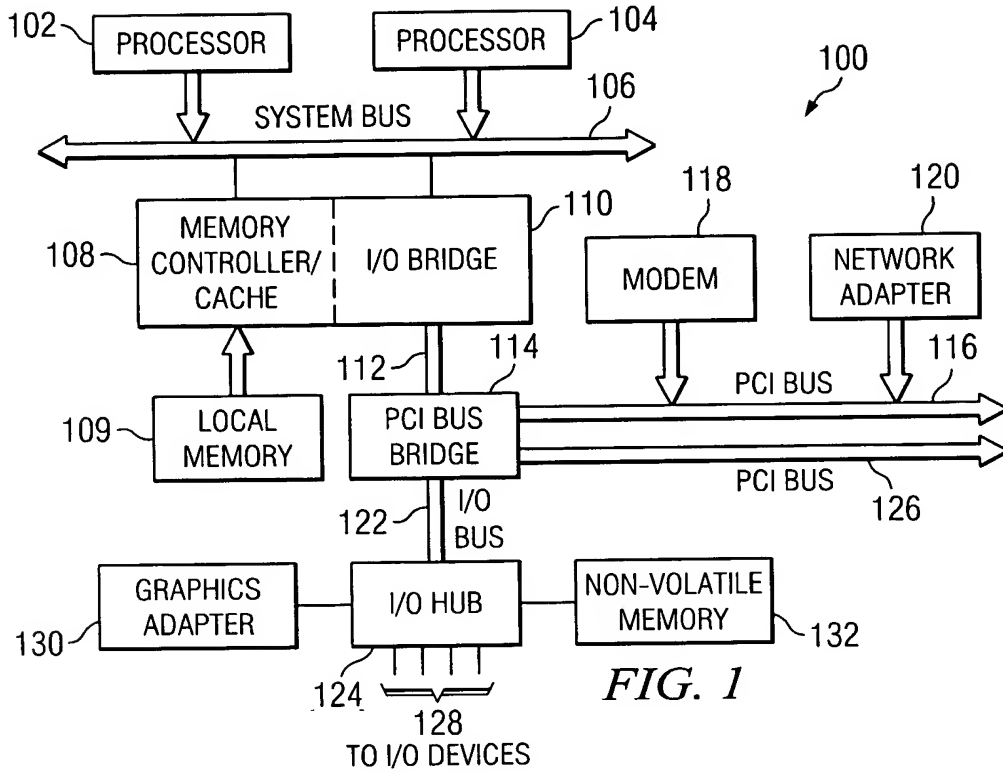


FIG. 1

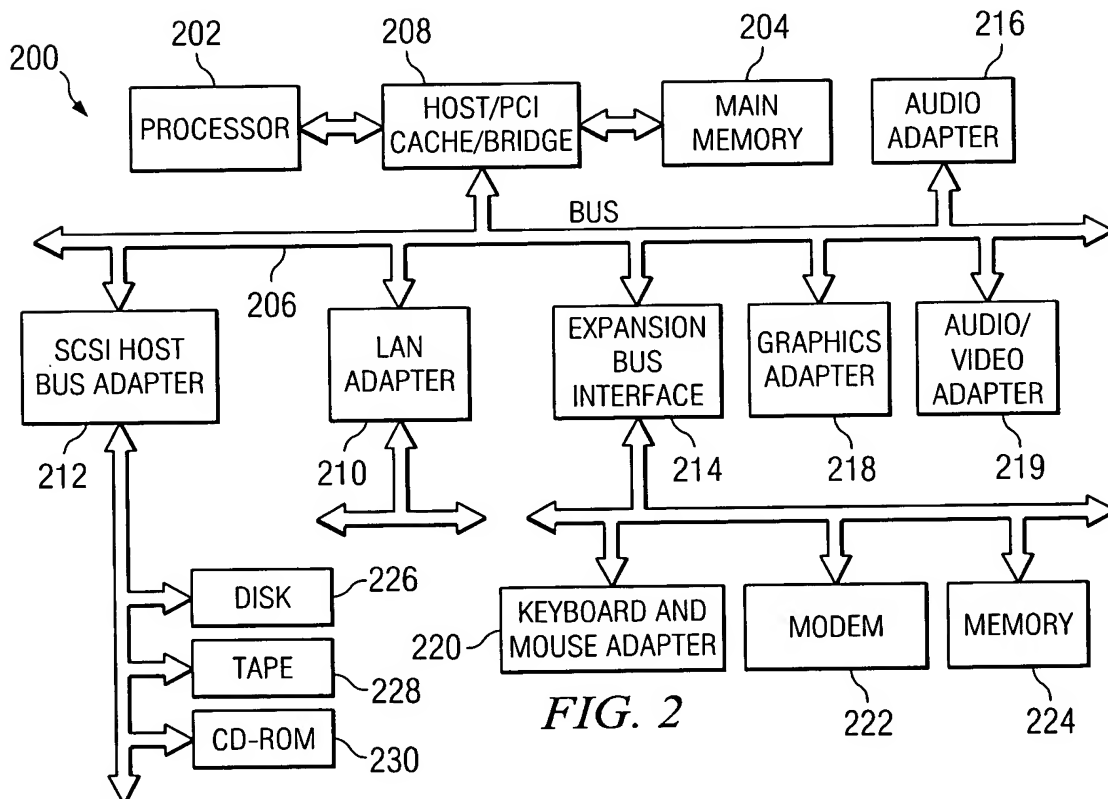


FIG. 2

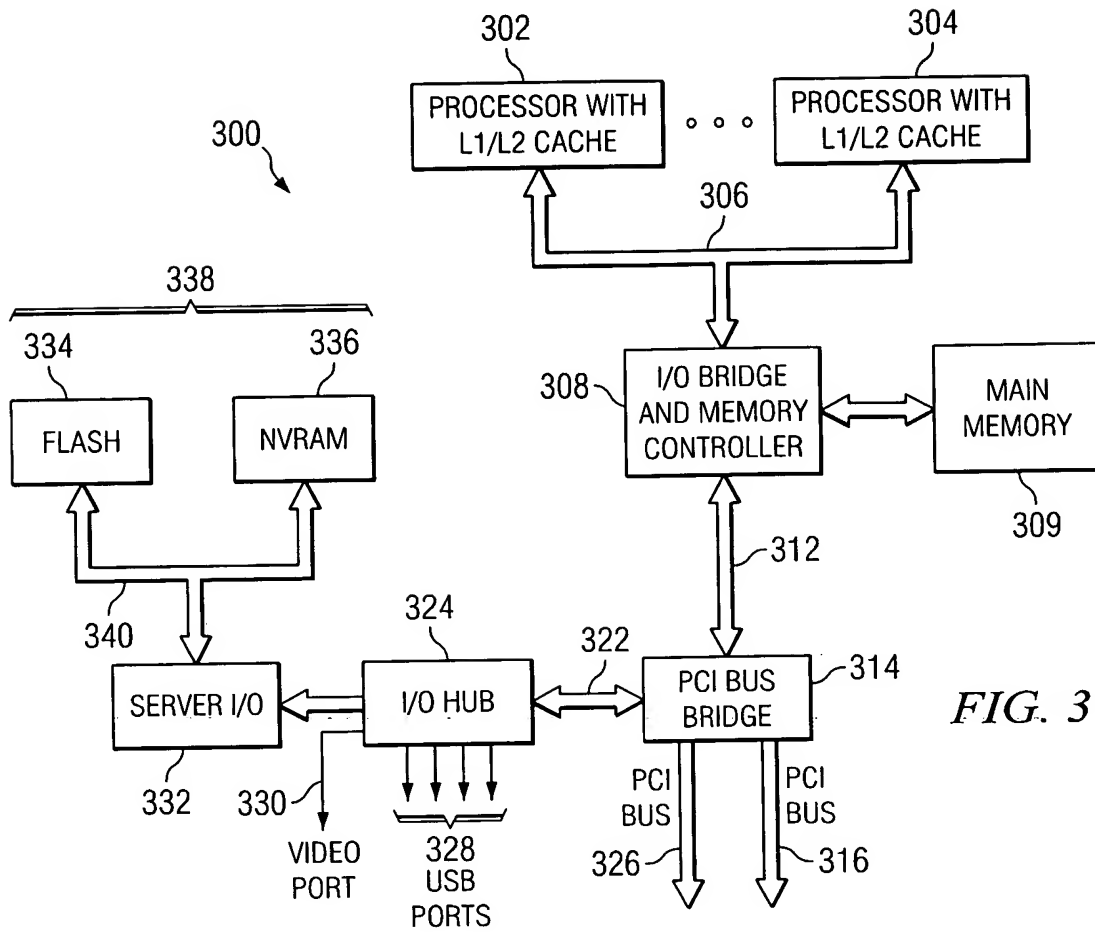


FIG. 3

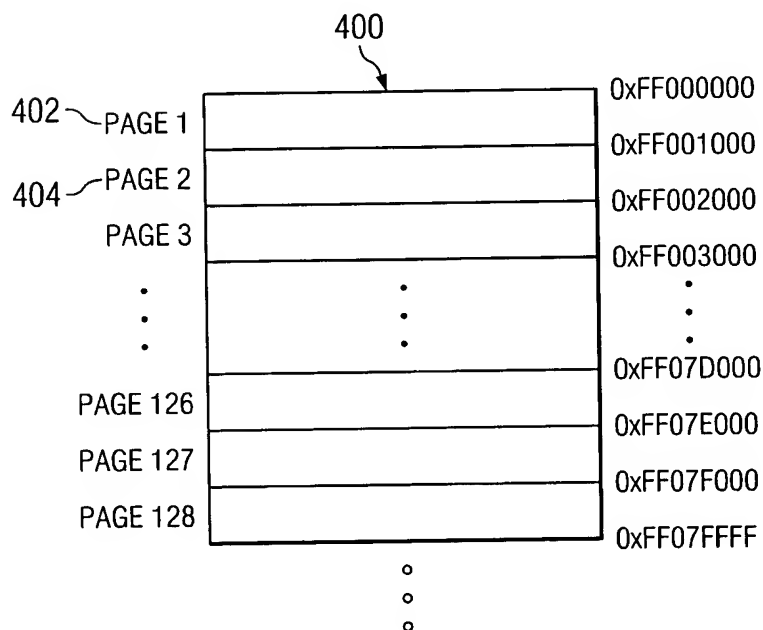
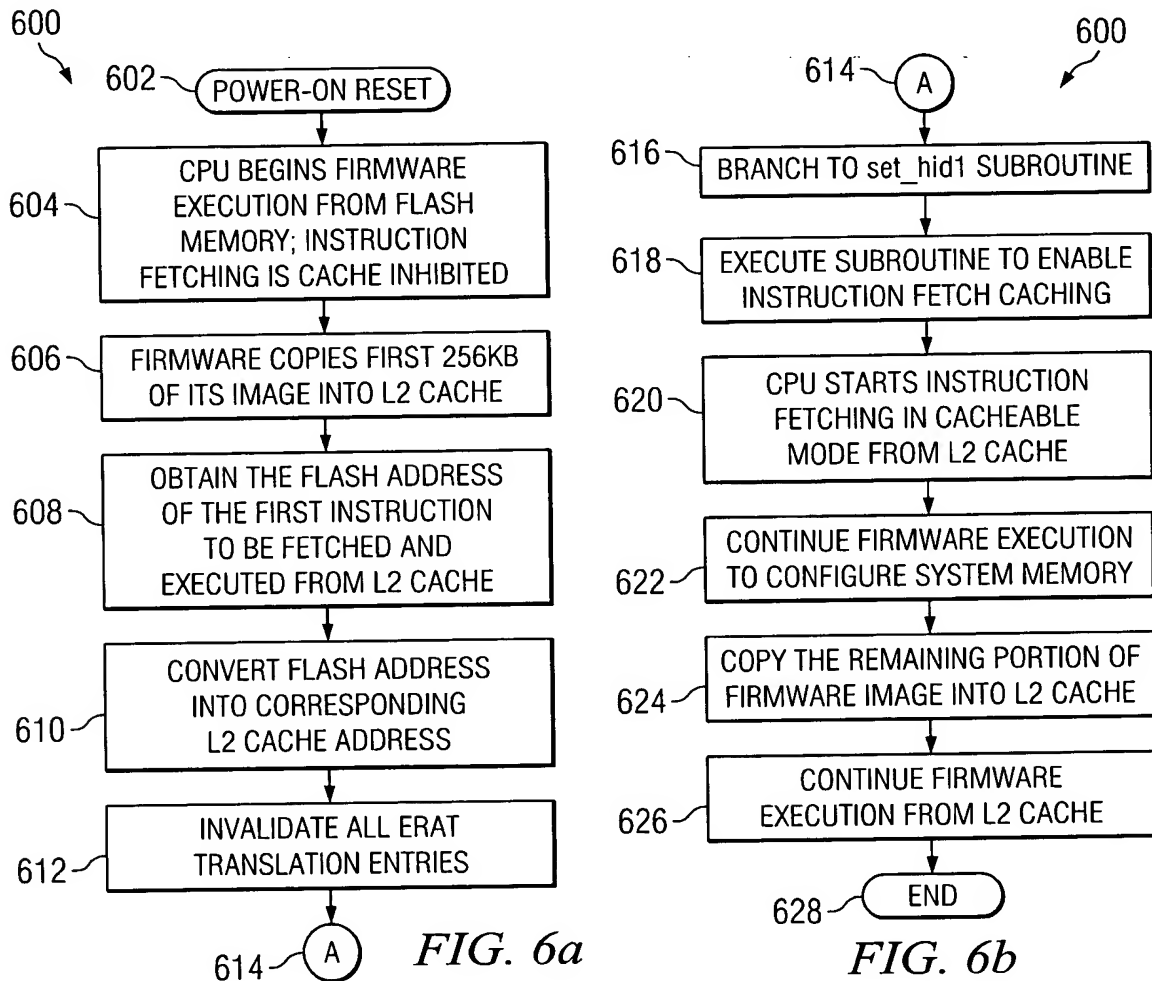
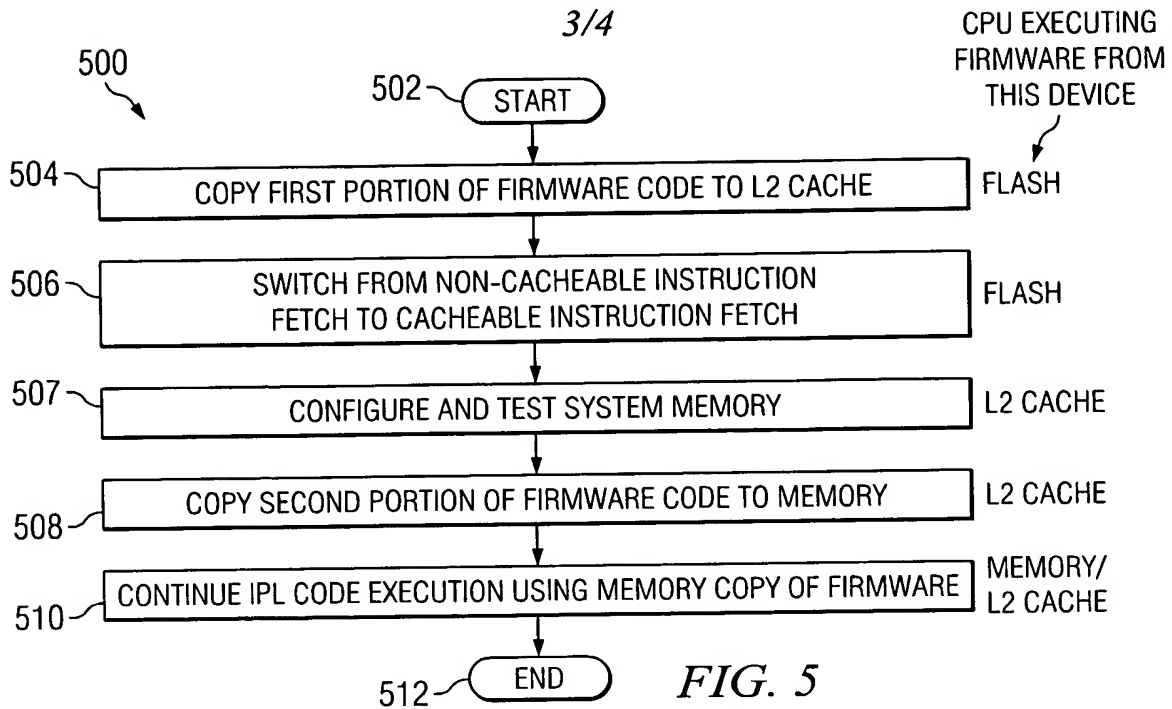


FIG. 4



Appendix A

Instruction Address Flow

Starting at 0xFF000100 from reset_interrupt_handler
After executing set_hid1 subroutine to enable instruction fetch cacheable
Cacheable instruction fetch continues at 0x00D05050

First two pages of the firmware image in flash

Routine to copy firmware code from flash into L2 cache

